

Chapter	Topic
LOGIC GATE	Logic GATE Part 1
	Logic GATE Part 2
	Logic GATE Part 3
MINIMIZATION OF FUNCTION	Boolean Algebra
	K MAP Part 1
	K MAP Part 2
	K MAP Part 3
COMBINATIONAL CIRCUIT	Comparator and Adder
	Subtractor and Serial Adder
	MUX & DEMUX
	ENCODER & DECODER
	Parallel Adder & LACA
SEQUENTIAL CIRCUIT	Multiplier & Miscellaneous
	Latches & Flip Flop Part 1
	Flip Flop Part 2
	Registers and Basic of Counters
	Counters Part 1
	Counters Part 2
	Counters Part 3
Number System	Number System