Chapter	Topic
LOGIC GATE	NOT GATE
	AND GATE, OR GATE
	NAND GATE, NOR GATE
	X OR, XNOR GATE
	NAND/NOR as a Universal LOGIC
	Questions Based on LOGIC GATE
MINIMZATION OF FUNCTION	Basic Terms of Functions
	Theorems
	Questions Based on Boolean Algebra
	K MAP Part 1
	K MAP Part 2
	K MAP Part 3
COMBINATIONAL CIRCUIT	Comparator
	MUX
	DEMUX & ENCODER
	DECODER
	Adder/ Subtractor
	Serial and Parallel LADDER
	LACA & Multiplier
SEQUENTIAL CIRCUIT	Latches
	Flip Flop Part 1
	Flip Flop Part 2
	Flip Flop Design
	Registers
	Counters Part 1
	Counters Part 2
	Counters Part 3
	Counters Part 4
Number System	Number System Part 1
	Number System Part 2