

COA	
Chapter Name	Topics Name
Introduction Of COA	Intoduction of COA & Instruction Cycle
Introduction Of COA	System Bus & Memory Concept
Introduction Of COA	Byte Ordering
Machine Instruction & Addressing Modes	Machine Instruction & Instruction Set Architecture
Machine Instruction & Addressing Modes	Expand Opcode Technique
Machine Instruction & Addressing Modes	Addressing Mode - Part I
Machine Instruction & Addressing Modes	Addressing Mode - Part II
Machine Instruction & Addressing Modes	Addressing Mode - Part III
Floating Point Representation	Floating Point Representation - Part I
Floating Point Representation	Floating Point Representation - Part II
Floating Point Representation	Floating Point Representation - Part III
ALU & Control Unit	Micro operation & Program
ALU & Control Unit	Micro operation & Program - Part II
ALU & Control Unit	Control Unit
ALU & Control Unit	CPU Time Calculation
Instruction Pipelining	Instruction Pipelining - Part I
Instruction Pipelining	Instruction Pipelining - Part II
Instruction Pipelining	Instruction Pipelining - Part III
Instruction Pipelining	Instruction Pipelining - Part IV
Instruction Pipelining	Instruction Pipelining - Part V
Instruction Pipelining	Instruction Pipelining - Part VI
Cache Memory	Cache Memory Concept
Cache Memory	Cache Mapping Technique - Part I
Cache Memory	Cache Mapping Technique - Part II
Cache Memory	Replacement Algo & Updaing Technique - Part I
Cache Memory	Replacement Algo & Updaing Technique - Part II
Cache Memory	Multi Level Cache
Secondary Memory & IO Interface	Disk Concept & Disk Access Time
Secondary Memory & IO Interface	Disk Addressing.
Secondary Memory & IO Interface	IO Interface